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## A few results on table-based methods

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# A few results on table-based methods 

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#### Abstract

Table-based methods are frequently used to implement functions. We examine some methods introduced in the literature, and we introduce a generalization of the bipartite table method, named the multipartite table method.


Keywords: Elementary functions, computer arithmetic, table-based methods

## Résumé

Les méthodes à base de tables sont de plus en plus utilisées pour implanter des fonctions. Nous examinons quelques méthodes suggérées antérieurement, puis nous proposons une généralisation de la méthode des "tables bipartites", appelée méthode des "tables multipartites".

Mots-clés: Fonctions élémentaires, arithmétique des ordinateurs, méthodes à base de tables.


#### Abstract

Table-based methods are frequently used to implement functions. We examine some methods introduced in the literature, and we introduce a generalization of the bipartite table method, named the multipartite table method.


## 1 Introduction

Throughout the paper, $f$ is the function to be evaluated. We assume $n$-bit, fixed-point arguments, between $1 / 2$ and 1 (that is, they are mantissas of floating-point numbers).

Table-based methods have frequently been suggested and used to implement some arithmetic (reciprocal, square root) and transcendental functions. One can distinguish three different classes of methods:

- compute-bound methods: these methods use table-lookup in a small table to find parameters used afterward for a polynomial or rational evaluation. The main part of the evaluation of $f$ consists in arithmetic computations;
- table-bound methods: The main part of the evaluation of $f$ consists in looking up in a generally rather large table. The computational part of the function evaluation is rather small (e.g., a few additions);
- in-between methods: these methods use the combination of table lookup in a medium-size table and a significant yet reduced amount of computation (e.g. one or two multiplications, or several "small multiplications" that use rectangular fast and/or small - multipliers).

Many methods currently used on general-purpose systems belong to the first class (e.g. Tang's methods [7, 8, 9, 10]. The third class of methods has been widely studied since 1981 [2]. The use of small (e.g., rectangular) multipliers to fasten the computational part of the evaluation has been suggested by several authors (see for instance Wong and Goto's algorithms for double precision calculations [11], or Ercegovac et al.'s methods [1]).

In this paper, we examine some table-bound methods. Of course, the straightforward method, consisting in building a table with $n$ address bits, cannot be used unless $n$ is very small. The first useful table-bound methods have been introduced in the last decade: they have become implementable thanks to progress in VLSI technology. Wong and Goto [12] have suggested the following method. We split the binary representation of the input-number $x$ into four $k$-bit numbers, where $k=n / 4$. That is, we write ${ }^{1}$ :

$$
=x_{1}+x_{2} 2^{-k}+x_{3} 2^{-2 k}+x_{4} 2^{-3 k}
$$

where $0 \leq x_{i} \leq 1-2^{-k}$ is a multiple of $2^{-k}$.
Then $f(x)$ is approximated by:

$$
\begin{aligned}
& f\left(x_{1}+x_{2} 2^{-k}\right) \\
& +\frac{1}{2} 2^{-k}\left\{f\left(x_{1}+x_{2} 2^{-k}+x_{3} 2^{-k}\right)-f\left(x_{1}+x_{2} 2^{-k}-x_{3} 2^{-k}\right)\right\} \\
& +\frac{1}{2} 2^{-2 k}\left\{f\left(x_{1}+x_{2} 2^{-k}+x_{4} 2^{-k}\right)-f\left(x_{1}+x_{2} 2^{-k}-x_{4} 2^{-k}\right)\right\} \\
& +2^{-4 k}\left\{\frac{x_{3}^{2}}{2} f^{(2)}\left(x_{1}\right)-\frac{x_{3}^{3}}{6} f^{(3)}\left(x_{1}\right)\right\}
\end{aligned}
$$

[^0]The approximation error due to the use of this approximation is about $2^{-5 k}$.
The bipartite table method was first suggested by Das Sarma and Matula [4] for quickly computing reciprocals. A slight improvement, the symmetric bipartite table method was introduced by Schulte and Stine [5]. Due to the importance of the bipartite table method (BTM), we will present it in detail in the next section. Compared to Wong and Goto's method, it requires larger tables. And yet, the amount of computation required by the BTM is reduced to one addition.

The problem of evaluating a function given by a converging series can be reduced to the evaluation of a partial product array (PPA). Schwarz [6] suggested to use multiplier structures to sum up PPAs. Hassler and Takagi [3] use PPAs to evaluate functions by table look-up and addition.

## 2 Order-1 methods

The methods described in this section use an order- 1 Taylor approximation of $f$. This leads to very simple computations (mere additions), but the size of the required tables may be quite large.

### 2.1 The bipartite table method

This method was first suggested by DasSarma and Matula [4] for computing reciprocals. We split the binary representation of the input number $x$ into $3 k$-bit numbers, where $k=n / 3$. That is, we write:

$$
=x_{1}+x_{2} 2^{-k}+x_{3} 2^{-2 k}
$$

where $0 \leq x_{i} \leq 1-2^{-k}$ is a multiple of $2^{-k}$.


We then write the order-1 Taylor expansion of $f$ at $x_{1}+x_{2} 2^{-k}$. This gives:

$$
\begin{equation*}
f(x)=f\left(x_{1}+x_{2} 2^{-k}\right)+x_{3} 2^{-2 k} f^{\prime}\left(x_{1}+x_{2} 2^{-k}\right)+\epsilon_{1} \tag{1}
\end{equation*}
$$

with $\epsilon_{1}=\frac{1}{2} x_{3}^{2} 2^{-4 k} f^{\prime \prime}\left(\xi_{1}\right)$, where $\xi_{1} \in\left[x_{1}+x_{2} 2^{-k}, x\right]$. Now, we approximate the value $f^{\prime}\left(x_{1}+x_{2} 2^{-k}\right)$ by its order-0 Taylor expansion at $x_{1}$ (that is, by $f^{\prime}(x 1)$. This gives:

$$
\begin{equation*}
f(x)=f\left(x_{1}+x_{2} 2^{-k}\right)+x_{3} 2^{-2 k} f^{\prime}\left(x_{1}\right)+\epsilon_{1}+\epsilon_{2} \tag{2}
\end{equation*}
$$

with $\epsilon_{2}=x_{2} x_{3} 2^{-3 k} f^{\prime \prime}\left(\xi_{2}\right)$, where $\xi_{2} \in\left[x_{1}, x_{1}+x_{2} 2^{-k}\right]$. This gives the bipartite formula:

$$
\begin{equation*}
f(x)=\alpha\left(x_{1}, x_{2}\right)+\beta\left(x_{1}, x_{3}\right)+\epsilon \tag{3}
\end{equation*}
$$

where

$$
\begin{cases}\alpha\left(x_{1}, x_{2}\right) & =f\left(x_{1}+x_{2} 2^{-k}\right) \\ \beta\left(x_{1}, x_{3}\right) & =x_{3} 2^{-2 k} f^{\prime}\left(x_{1}\right) \\ \epsilon & \leq\left(\frac{1}{2} 2^{-4 k}+2^{-3 k}\right) \max f^{\prime \prime} \approx 2^{-3 k} \max f^{\prime \prime}\end{cases}
$$



Figure 1: The bipartite table method

Hence, $f(x)$ can be approximated, with approximately $n$ bits of accuracy (by this, we mean "with error $\approx 2^{-n}$ ") by the sum of two terms ( $\alpha$ and $\beta$ ) that can be looked-up in $2 n / 3$-address bit tables, as illustrated by Figure 1.

The BTM still leads to large tables in single precision, and this. It is far from being implementable in double precision. And yet, this leads to another idea: we should try to generalize the bipartite method, by splitting the input word into more than three parts. Let us first try a splitting into five parts, we will after that generalize to an arbitrary odd number of parts.

### 2.2 The tripartite table method

Now, we split the input $n$-bit fixed-point number $x$ into five $k$-bit parts $x_{1}, x_{2}, \ldots$, $x_{5}$. That is, we write:

$$
=x_{1}+x_{2} 2^{-k}+x_{3} 2^{-2 k}+x_{4} 2^{-3 k}+x_{5} 2^{-4 k}
$$

where $0 \leq x_{i} \leq 1-2^{-k}$ is a multiple of $2^{-k}$.


We use the order-1 Taylor expansion of $f$ at $=x_{1}+x_{2} 2^{-k}+x_{3} 2^{-2 k}$ :

$$
\begin{gather*}
f(x)=f\left(x_{1}+x_{2} 2^{-k}+x_{3} 2^{-2 k}\right) \\
+\left(x_{4} 2^{-3 k}+x_{5} 2^{-4 k}\right) f^{\prime}\left(x_{1}+x_{2} 2^{-k}+x_{3} 2^{-2 k}\right)  \tag{4}\\
+\epsilon_{3}+\epsilon_{1}
\end{gather*}
$$

with $\epsilon_{3}=\frac{1}{2}\left(x_{4} 2^{-3 k}+x_{5} 2^{-4 k}\right)^{2} f^{\prime \prime}\left(\xi_{3}\right)$, with $\xi_{3} \in\left[x_{1}+x_{2} 2^{-k}+x_{3} 2^{-2 k}, x\right]$, which gives $\epsilon_{3} \leq \frac{1}{2} 2^{-6 k} \max f^{\prime \prime}$.

In (4), we expand the term $\left(x_{4} 2^{-3 k}+x_{5} 2^{-4 k}\right) f^{\prime}\left(x_{1}+x_{2} 2^{-k}+x_{3} 2^{-2 k}\right)$ as follows:


Figure 2: The tripartite table method

- $x_{4} 2^{-3 k} f^{\prime}\left(x_{1}+x_{2} 2^{-k}+x_{3} 2^{-2 k}\right)$ is replaced by $x_{4} 2^{-3 k} f^{\prime}\left(x_{1}+x_{2} 2^{-k}\right)$. The error committed is $\epsilon_{4}=x_{3} x_{4} 2^{-5 k} f^{\prime \prime}\left(\xi_{4}\right)$, where $\xi_{4} \in\left[x_{1}+x_{2} 2^{-k}, x_{1}+x_{2} 2^{-k}+x_{3} 2^{-2 k}\right]$. We easily get $\epsilon_{4} \leq 2^{-5 k} \max f^{\prime \prime}$.
- $x_{5} 2^{-4 k} f^{\prime}\left(x_{1}+x_{2} 2^{-k}+x_{3} 2^{-2 k}\right)$ is replaced by $x_{5} 2^{-4 k} f^{\prime}\left(x_{1}\right)$. The error committed is $\epsilon_{5}=\left(x_{2} 2^{-k}+x_{3} 2^{-2 k}\right) x_{5} 2^{-4 k} f^{\prime \prime}\left(\xi_{5}\right)$, where $\xi_{5} \in\left[x_{1}, x_{1}+x_{2} 2^{-k}+x_{3} 2^{-2 k}\right]$. We get $\epsilon_{5} \leq 2^{-5 k} \max f^{\prime \prime}$.
This gives the tripartite formula:

$$
\begin{equation*}
f(x)=\gamma\left(x_{1}, x_{2}, x_{3}\right)+\delta\left(x_{1}, x_{2}, x_{4}\right)+\theta\left(x_{1}, x_{5}\right)+\epsilon \tag{5}
\end{equation*}
$$

where

$$
\begin{array}{ll}
\gamma\left(x_{1}, x_{2}, x_{3}\right) & =f\left(x_{1}+x_{2} 2^{-k}+x_{3} 2^{-2 k}\right) \\
\delta\left(x_{1}, x_{2}, x_{4}\right) & =x_{4} 2^{-3 k} f^{\prime}\left(x_{1}+x_{2} 2^{-k}\right) \\
\theta\left(x_{1}, x_{5}\right) & =x_{5} 2^{-4 k} f^{\prime}\left(x_{1}\right) \\
\epsilon & \leq\left(\frac{1}{2} 2^{-6 k}+2 \times 2^{-5 k}\right) \max f^{\prime \prime} \approx 2^{-5 k+1} \max f^{\prime \prime}
\end{array}
$$

Hence, $f(x)$ can be obtained by adding three terms, each of them being looked-up in a table with (at most) $3 n / 5$ address bits. This is illustrated by Figure 2.

### 2.3 Generalization: the multipartite table method

The previous approach is straightforwardly generalized. We now assume that the $n$-bit input number $x$ is split into $2 p+1 k$-bit values $x_{1}, x_{2}, \ldots x_{2 p+1}$. That is,

$$
x=\sum_{i=1}^{2 p+1} x_{i} 2^{(i-1) k}
$$

where the $x_{i}$ 's are multiples of $2^{-k}$ and satisfy $0 \leq x_{i}<1$. As in the previous sections, we use the order-1 Taylor expansion:

$$
\begin{aligned}
f(x) & =f\left(x_{1}+x_{2} 2^{-k}+\ldots+x_{p+1} 2^{-p k}\right) \\
& +\left(x_{p+2} 2^{(-p-1) k}+\ldots+x_{2 p+1} 2^{-2 p k}\right) f^{\prime}\left(x_{1}+x_{2} 2^{-k}+\ldots+x_{p+1} 2^{-p k}\right) \\
& +\epsilon_{p+1}
\end{aligned}
$$

with $\epsilon_{p+1} \leq \frac{1}{2} 2^{-2(p+1) k} \max f^{\prime \prime}$. We expand the term

$$
\left(x_{p+2} 2^{(-p-1) k}+\ldots+x_{2 p+1} 2^{-2 p k}\right) f^{\prime}\left(x_{1}+x_{2} 2^{-k}+\ldots+x_{p+1} 2^{-p k}\right)
$$

and perform Taylor approximations to $f^{\prime}\left(x_{1}+x_{2} 2^{-k}+\ldots+x_{p+1} 2^{-p k}\right)$. We then get:

$$
\begin{aligned}
f(x) & =f\left(x_{1}+x_{2} 2^{-k}+\ldots+x_{p+1} 2^{-p k}\right)+\epsilon_{p+1} \\
& +x_{p+2} 2^{(-p-1) k} f^{\prime}\left(x_{1}+x_{2} 2^{-k}+\ldots+x_{p} 2^{(-p+1) k}\right)+\epsilon_{p+2} \\
& +x_{p+3} 2^{(-p-2) k} f^{\prime}\left(x_{1}+x_{2} 2^{-k}+\ldots+x_{p-1} 2^{(-p+2) k}\right)+\epsilon_{p+3} \\
& +x_{p+4} 2^{(-p-3) k} f^{\prime}\left(x_{1}+x_{2} 2^{-k}+\ldots+x_{p-2} 2^{(-p+3) k}\right)+\epsilon_{p+4} \\
& \ldots \\
& +x_{2 p+1} 2^{-2 p k} f^{\prime}\left(x_{1}\right)+\epsilon_{2 p+1}
\end{aligned}
$$

where $\epsilon_{p+2}, \epsilon_{p+3}, \ldots \epsilon_{2 p+1}$ are less than $2^{(-2 p-1) k} \max f^{\prime \prime}$.
This gives the multipartite (or ( $p+1$ )-partite) formula:

$$
\begin{align*}
f(x) & =\alpha_{1}\left(x_{1}, x_{2}, \ldots, x_{p+1}\right) \\
& +\alpha_{2}\left(x_{1}, x_{2}, \ldots, x_{p-1}, x_{p+3}\right) \\
& +\alpha_{3}\left(x_{1}, x_{2}, \ldots, x_{p-2}, x_{p+4}\right) \\
& +\alpha_{4}\left(x_{1}, x_{2}, \ldots, x_{p-3}, x_{p+5}\right)  \tag{6}\\
& +\alpha_{p+1}\left(x_{1}, x_{2 p+1}\right) \\
& +\epsilon
\end{align*}
$$

where

$$
\begin{cases}\alpha_{1}\left(x_{1}, \ldots, x_{p+1}\right) & =f\left(x_{1}+x_{2} 2^{-k}+\ldots+x_{p+1} 2^{-p k}\right) \\ \alpha_{i}\left(x_{1}, \ldots, x_{p-i+2}, x_{p+i}\right) & =x_{p+i} 2^{-p-i+1} f^{\prime}\left(x_{1}+x_{2} 2^{-k}+\ldots+x_{p-i+2} 2^{(-p+i-1) k}\right) \\ \epsilon & \leq\left(\frac{1}{2} 2^{(-2 p-2) k}+p 2^{(-2 p-1) k}\right) \max f^{\prime \prime} \\ & \approx p 2^{(-2 p-1) k} \max f^{\prime \prime}\end{cases}
$$

Too large values of $p$ are unrealistic: performing many additions to avoid a few multiplications is not reasonable.

## 3 Higher-order methods

In the previous section, we have used order-1 Taylor expansions only. Now, let us give an example of the use of an order-2 expansion. As in section 2.2 , we split the input $n$-bit fixed-point number $x$ into five $k$-bit parts $x_{1}, x_{2}, \ldots, x_{5}$. That is, we write:

$$
=x_{1}+x_{2} 2^{-k}+x_{3} 2^{-2 k}+x_{4} 2^{-3 k}+x_{5} 2^{-4 k}
$$

| $2 p+1$ | n | nb bytes | nb of address bits |
| :--- | :--- | :--- | :--- |
| bipartite | 24 | 262144 | 16 |
| bipartite (larger $n$ ) | 27 | 1179648 | 18 |
| tripartite | 25 | 143360 | 15 |
| tripartite (larger $n$ ) | 30 | 1376256 | 18 |
| $2 p+1=7$ | 28 | 327680 | 16 |
| $2 p+1=7$ (larger $n)$ | 35 | 6553600 | 20 |

Table 1: Table sizes for various order-1 methods
where $0 \leq x_{i} \leq 1-2^{-k}$ is a multiple of $2^{-k}$.

$$
\begin{align*}
\alpha_{1}\left(x_{1}, x_{2}\right) & =f\left(x_{1}+x_{2} 2^{-k}\right)-3 f\left(x_{1}\right)-\frac{1}{2}\left(2^{-3 k}+2^{-4 k}\right) x_{2}^{2} f^{\prime \prime}\left(x_{1}\right) \\
\alpha_{2}\left(x_{1}, x_{3}\right) & =f\left(x_{1}+x_{3} 2^{-2 k}\right)-\frac{1}{2} 2^{-3 k} x_{3}^{2} f^{\prime \prime}\left(x_{1}\right)-\frac{1}{6} 2^{-4 k} x_{3}^{3} f^{\prime \prime \prime \prime}\left(x_{1}\right) \\
\alpha_{3}\left(x_{1}, x_{4}\right) & =f\left(x_{1}+x_{4} 2^{-3 k}\right)-\frac{1}{2} 2^{-4 k} x_{4}^{2} f^{\prime \prime}\left(x_{1}\right) \\
u & =x_{2}+x_{3}  \tag{7}\\
v & =x_{2}-x_{3} \\
\beta_{1}\left(u, x_{1}\right) & =\frac{1}{12} 2^{-4 k} u^{3} f^{\prime \prime \prime \prime}\left(x_{1}\right) \\
\beta_{2}\left(v, x_{1}\right) & =-\frac{1}{12} 2^{-4 k} v^{3} f^{\prime \prime \prime}\left(x_{1}\right)
\end{align*}
$$

Then

$$
f(x) \approx \alpha_{1}\left(x_{1}, x_{2}\right)+\alpha_{2}\left(x_{1}, x_{3}\right)+\alpha_{3}\left(x_{1}, x_{4}\right)+\beta_{1}\left(u, x_{1}\right)+\beta_{2}\left(v, x_{1}\right)
$$

Hence, with this method, we can use tables with $2 n / 5$ address bits. Two additions are used to generate $u$ and $v$, and after the table-lookups, two carry-save additions and one carry-propagate addition suffice to get the final result. This method requires around 15 K bytes of table for single-precision.

## Conclusion

Various table-based methods have been suggested during the last decade. When singleprecision implementation is at stake, table-bound methods seem to be agood candidate for implementing fast functions. Unless there is a technology breakthrough, these methods are not suitable for double precision.

## References

[1] M.D. Ercegovac, T. Lang, J.M. Muller, and A. Tisserand. Reciprocation, square root, inverse square root, and some elementary functions using small multipliers. Technical Report RR97-47, LIP, École Normale Supérieure de Lyon, November 1997. available at ftp://ftp.lip.ens-lyon.fr/pub/Rapports/RR/RR97/RR9747.ps.Z.
[2] P. M. Farmwald. High bandwidth evaluation of elementary functions. In In K. S. Trivedi and D. E. Atkins, editors, Proceedings of the 5th IEEE Symposium on Computer Arithmetic. IEEE Computer Society Press, Los Alamitos, CA, 1981.
[3] H. Hassler and N. Takagi. Function evaluation by table look-up and addition. In S. Knowles and W. McAllister, editors, Proceedings of the 12 th IEEE Symposium on Computer Arithmetic, Bath, UK, July 1995. IEEE Computer Society Press, Los Alamitos, CA.
[4] D. Das Sarma and D. W. Matula. Faithful bipartite rom reciprocal tables. In S. Knowles and W. H. McAllister, editors, Proceedings of the 12th IEEE Symposium on Computer Arithmetic, pages 17-28, Bath, UK, 1995. IEEE Computer Society Press, Los Alamitos, CA.
[5] M. Schulte and J. Stine. Symmetric bipartite tables for accurate function approximation. In In T. Lang, J.M. Muller, and N. Takagi, editors, Proceedings of the 13th IEEE Symposium on Computer Arithmetic. IEEE Computer Society Press, Los Alamitos, CA, 1997.
[6] E. Schwarz. High-Radix Algorithms for High-Order Arithmetic Operations. PhD thesis, Dept. of Electrical Engineering, Stanford University, 1992.
[7] P. T. P. Tang. Table-driven implementation of the exponential function in IEEE floating-point arithmetic. ACM Transactions on Mathematical Software, 15(2):144-157, June 1989.
[8] P. T. P. Tang. Table-driven implementation of the logarithm function in IEEE floating-point arithmetic. ACM Transactions on Mathematical Software, 16(4):378-400, December 1990.
[9] P. T. P. Tang. Table lookup algorithms for elementary functions and their error analysis. In P. Kornerup and D. W. Matula, editors, Proceedings of the 10th IEEE Symposium on Computer Arithmetic, pages 232-236, Grenoble, France, June 1991. IEEE Computer Society Press, Los Alamitos, CA.
[10] P. T. P. Tang. Table-driven implementation of the expm1 function in IEEE floating-point arithmetic. ACM Transactions on Mathematical Software, 18(2):211-222, June 1992.
[11] W. F. Wong and E. Goto. Fast hardware-based algorithms for elementary function computations using rectangular multipliers. IEEE Transactions on Computers, 43(3):278-294, March 1994.
[12] W. F. Wong and E. Goto. Fast evaluation of the elementary functions in single precision. IEEE Transactions on Computers, 44(3):453-457, March 1995.


[^0]:    ${ }^{1}$ To make the paper easier to read and more consistent, we do not use Wong and Goto's notations here. We use the same notations as in the sequel of this paper.

